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## UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No. 004198.P002

Total Pages 2

First Named Inventor or Application Identifier Ran Ginosar

Express Mail Label No. EL034433306US

ADDRESS TO: Assistant Commissioner for Patents  
Box Patent Application  
Washington, D. C. 20231

### APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent application contents.

1. ☒ Fee Transmittal Form  
(Submit an original, and a duplicate for fee processing)
2. ☒ Specification (Total Pages 28)  
(preferred arrangement set forth below)
  - Cover Sheet
  - Descriptive Title of the Invention
  - Cross References to Related Applications
  - Statement Regarding Fed sponsored R & D
  - Reference to Microfiche Appendix
  - Background of the Invention
  - Brief Summary of the Invention
  - Brief Description of the Drawings (if filed)
  - Detailed Description
  - Claims
  - Abstract of the Disclosure
3. ☒ Drawings(s) (35 USC 113) (Total Sheets 6)
4. ☒ Oath or Declaration/Power of Attorney (Total Pages 3)
  - a. ☒ Newly Executed (Original or Copy)
  - b. ☐ Copy from a Prior Application (37 CFR 1.63(d))  
(for Continuation/Divisional with Box 17 completed) (**Note Box 5 below**)
  - i. ☐ DELETIONS OF INVENTOR(S) Signed statement attached deleting inventor(s) named in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b).
5. ☐ Incorporation By Reference (useable if Box 4b is checked)  
The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4b, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.
6. ☐ Microfiche Computer Program (Appendix)
7. ☐ Nucleotide and/or Amino Acid Sequence Submission  
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12/01/97

Applicant or Patentee: Ran Ginosar Attorney's Docket No.: 004198.P002  
Serial or Patent No.: not yet assigned  
Filed or Issued: September 21, 1999  
For: A DUAL FORM LOW POWER, INSTANT ON AND HIGH PERFORMANCE,  
NON-INSTANT ON COMPUTING DEVICE

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**37 CFR 1.9 (f) and 1.27(c) - - SMALL BUSINESS CONCERN**

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ADDRESS OF CONCERN: 15455 N.W. Greenbrier Pkwy., Ste. 210  
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by inventor(s) Ran Ginosar  
described in

- ☒ the specification being filed herewith  
☐ application serial no. \_\_\_\_\_, filed \_\_\_\_\_  
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NAME OF PERSON SIGNING: Ben Ginosar

TITLE OF PERSON OTHER THAN OWNER: Vice President

ADDRESS OF PERSON SIGNING: 15455 N.W. Greenbrier Pkwy., Ste. 210, Beaverton, OR 97008

SIGNATURE: 

DATE: 9/21/99

## UNITED STATES PATENT APPLICATION

for

**A DUAL FORM LOW POWER, INSTANT ON AND HIGH PERFORMANCE, NON-  
INSTANT ON COMPUTING DEVICE**

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File No.: 004198.P002

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**A DUAL FORM LOW POWER, INSTANT ON AND HIGH  
PERFORMANCE, NON-INSTANT ON COMPUTING DEVICE**

**BACKGROUND OF THE INVENTION**

1. **FIELD OF THE INVENTION**

5           The present invention relates to the field of computing in general and, in particular, to a dual form low power, instant on and high performance, non-instant on computing device.

2. **BACKGROUND INFORMATION**

10           Desktop computing devices have typically provided the most processing power and largest data storage capabilities available on the market for the least amount of money. At least, that is true when compared to other computing devices such as laptop or notebook computers. It has only been recently, with the proliferation of low cost computing components such as processors, memory, and magnetic storage devices, however, that laptop computers have been able to approach the price versus  
15           performance barrier that desktop computers have traditionally defined. The slightly higher price that an equivalently equipped laptop computer demands over its desktop counterpart is considered by some to be an acceptable tradeoff when such factors as portability are factored in.

20           Unfortunately, price is sometimes less of a concern than expected battery life when it comes to potential laptop purchasers. Today, the average battery life expectancy for a laptop computer tends to be only 2 to 4 hours, thereby limiting the time and distance one may travel without recharging the battery. Additionally, the battery life expectancy of a laptop tends to be inversely proportional to the number of peripheral

devices, such as CD/DVD-ROM drives, MODEMS, and various PC cards connected to the laptop. As the number of peripheral devices connected to the laptop increases, the resulting battery life of the laptop can be expected to decrease.

Most recently, a new genre of low power portable computing devices have entered the market. These portable devices, sometimes referred to as handhelds, or palm-size computers, are typically smaller and more lightweight than laptop computers, have a battery life expectancy that far surpasses that of laptops, and provide "instant on" access to programs and data, rather than requiring an extended operating system load time upon startup as do laptops. Typically, when these handheld devices are powered-on, a microprocessor contained within the device immediately begins to execute operating system instructions and applications stored within a read only memory (ROM) and/or random access memory (RAM) device. Since these instructions do not need to be loaded from a relatively slow data storage device, such as a hard disk drive, prior to execution by the microprocessor, startup time is decreased and battery life increased. Unfortunately, however, as convenient as these handheld devices may seem, they do have some drawbacks.

Whereas desktop and laptop computers provide an abundance of data storage space, handheld devices remain limited in the amount of data that may be stored within the device. External data transfer cables may be used to transfer data between a handheld computing device and an external data storage device, however, such transfer mechanisms tend to be awkward and time consuming. Similarly, handheld devices typically require specially compiled versions of applications software and

operating systems that may only provide limited functionality as compared to a version generally available for laptops, e.g. Microsoft WINDOWS CE vs. WINDOWS 98.

Additionally, because of their reduced size, handheld devices typically contain smaller video displays as compared to laptops, and such displays may or may not provide color display capability. Furthermore, not all handheld devices contain keyboards, and those that do, often implement reduced size micro-keyboards or keypads, rather than the larger size keyboards available on most laptops.

Some users have the need for both a low power handheld computing device as well as a high performance laptop computer depending upon their particular circumstances. For example, when travelling in an airplane, a person may prefer to use a handheld computing device due to the long battery life. Likewise, a person may prefer to use a handheld computing device during a business meeting, where the use of a laptop computer may not be convenient. On the other hand, a high performance laptop computer may be preferable in situations where a user may have a large amount of data to input and has access to a power outlet. For example, a business person may use a laptop as their office computer, but may also choose to use a handheld computing device for use during travel and/or meetings. Such a user is therefore burdened with both the cost and inconvenience of two devices.

Therefore, what is needed is a single portable computing device that incorporates the advantages of both laptops and handheld devices, while overcoming the limitations of each found within the prior art.



**SUMMARY OF THE INVENTION**

An apparatus is provided with a first plurality of electronic components defining an instant on computing device for use in a first instant on mode of operation. The apparatus is further provided with a second plurality of electronic components, including a plurality of input and output devices defining a non-instant on computing device for use in a second non-instant on mode of operation. The apparatus is further provided with one or more switching mechanisms coupled to the input and output devices and to selected ones of the first and second plurality of electronic components to enable the input and output devices to be available for use in the first instant on, as well as the second non-instant on modes of operation.

**BRIEF DESCRIPTION OF THE DRAWINGS**

The invention is illustrated by way of example, and not by way of limitation in the figures of the accompanying drawings in which like reference numerals refer to similar elements.

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**Figure 1** is an architectural block diagram illustrating a conventional handheld computing device connected to a conventional laptop computer.

**Figure 2** is a block diagram illustrating a dual form computing device architecture according to one embodiment of the present invention.

**Figure 3** is a block diagram illustrating an add-on card implementation of a dual form computing device according to one embodiment of the present invention.

**Figure 4** is a block diagram illustrating a system-on-a-chip implementation of a dual form computing device according to one embodiment of the present invention.

**Figure 5** is a block diagram illustrating an integrated circuit (IC) implementation of a dual form computing device according to one embodiment of the of the present invention.

**Figure 6** is a block diagram illustrating one embodiment of the dual form computing device of the present invention including a single, dual mode microprocessor.

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**DETAILED DESCRIPTION**

A dual form low power, instant on and high performance, non-instant on computing device is disclosed. In the following description, for purposes of explanation, numerous specific details are set forth in order to provide a thorough understanding of the invention. It will be apparent, however, to one skilled in the art that the invention can be practiced without these specific details. In other instances, structures and devices are shown in block diagram form in order to avoid obscuring the invention.

Reference in the specification to "one embodiment" or "an embodiment" means that a particular feature, structure, or characteristic described in connection with the embodiment is included in at least one embodiment of the invention. The appearances of the phrase "in one embodiment" in various places in the specification are not necessarily all referring to the same embodiment.

Figure 1 is a block diagram illustrating a conventional handheld computing device connected to a conventional laptop computer. Referring to Figure 1, laptop computer 100 represents a general purpose portable computing device including microprocessor 102, main memory 104, data storage device 106, user input device 108, display device 110, input/output port 112, and communication device 114.

Microprocessor 102 represents various processors known in the art to process data signals. Main memory 104 represents a volatile data storage device, such as dynamic random access memory (DRAM), that stores instructions and/or data to be processed by microprocessor 102. Data storage device 106 represents a non-volatile

data storage device, such as a disk drive, CD-ROM, equipped to store large amounts of data for an extended period of time.

User input device 108 represents a "full function" or "full size" input device, such as a "full size" keyboard, mouse, or trackball that is equipped to convert user input into electronic data signals to be processed by microprocessor 102. Display device 110 represents a "full size" video display device, such as a 15" thin film transistor (TFT), active matrix or the like, for displaying information to a user. Communication device 114 represents an input/output communications device equipped to transfer data to and from laptop computer 100, such as a parallel interface, and I/O 112 represents additional input and output ports also equipped to transmit data to and from laptop computer 100.

In a conventional laptop boot sequence, laptop computer 100 will typically perform a power-on self test (POST) when power is applied. The POST routine comprises code typically stored in a non-volatile memory such as ROM (not shown) that verifies the integrity and configuration of laptop computer 100. Once the POST routine has completed successfully, laptop computer 100 begins to load operating system 116 from data storage device 106 into main memory 104 to be executed by microprocessor 102. Operating system 116 comprises instructions that when processed by microprocessor 102, manages the resources of laptop computer 100 enabling it to function in a conventional manner. As operating system 116 is loaded, various device drivers 118 may also be loaded. Device drivers 118 represent various instructions and data that when executed, facilitate access to the corresponding devices by operating system 116 and applications 120. Once operating system 116 and device drivers 118

(if any) have been loaded and executed, applications 120 may also be loaded and executed.

Handheld computing device 125 is shown coupled to laptop computer 100 by external cable 150. Handheld computing device 125 represents a handheld or palm-sized, limited function computing device, and includes microprocessor 127, main  
5 memory 129, flash memory 131, communication device 135, display device 137, user input device 139, operating system 141, and applications 143.

Microprocessor 127 represents various processors known in the art to process data signals. Main memory 129 and flash memory 131 each represent memory storage  
10 devices that store instructions and/or data that may be executed by microprocessor 127. Main memory 129 may comprise volatile RAM and/or nonvolatile ROM, whereas flash memory 131 may comprise a nonvolatile memory device that can be erased and reprogrammed.

Communication device 135 represents an input/output communications device  
15 equipped to transfer data to and from handheld computing device 125, such as a parallel interface. User input device 139 represents an input device, such as a stylus or reduced size keyboard, that is equipped to convert user input into electronic data signals to be processed by microprocessor 127. Display device 137 represents a small display device, such as a 4" liquid crystal display (LCD), for displaying data to a user.

20 In addition to various hardware components, handheld computing device 125 also contains various software components including operating system 141 and applications 143. Operating system 141 comprises instructions that when processed by microprocessor 127, manage the resources of computing device 125 to function in a

conventional manner. Operating system 141 represents any one of the various operating systems available for use with handheld computing device 125, including for example, WINDOWS CE™ from Microsoft Corporation of Redmond, Washington.

Operating system 141 provides system services for applications 143 to access the various hardware components described above.

Applications 143 may include various utility and data processing software programs that have been specially designed for handheld computing device 125, or have been recompiled to function with microprocessor 127. Often, applications 143 represent pared-down versions of application software commonly available for full-sized computing devices. Such pared-down, "light" or "pocket" versions typically contain only a subset of the functions available in the larger, more ubiquitous versions of the software. Both applications 143 and operating system 141 may reside in flash memory 131 and/or main memory 129 to be executed in place by microprocessor 127.

When operating conventionally, handheld computing device 125 functions in a low power, "instant on" manner, whereby when it is powered-on, microprocessor 127 executes instructions representing operating system 141 and applications 143 directly from main memory 129 and flash memory 131. In such a manner, handheld computing device 125 is able to bypass a long bootstrap and operating system load by executing operating instructions in place rather than loading the instructions into RAM from a comparatively slow data storage device, such as data storage device 106, prior to execution. In addition, handheld computing device 125 is able to bypass a long bootstrap and operating system load partially because operating system 141 contains only a small portion of the code typically contained within operating systems used by

larger, more robust, and more complex computing devices, such as laptop computer 100.

Additionally, when operating conventionally, handheld computing device 125 also functions in a low power mode, whereby microprocessor 127 operates at low frequency and supply voltage levels, executes a simple operating system that does not incur heavy overhead, and employs and powers only a limited set of peripheral I/O devices. In comparison, laptop computer 100 operates under greater power requirements, but may also achieve greater performance due to a higher operational frequency and voltage, in addition to executing a more complicated operating system.

Once handheld computing device 125 is powered-on and functioning under operating system 141, a user may input and store data in main memory 129, for example, using applications 143 and user input device 139. Data stored within handheld computing device 125 may be accessed using applications 143, or transferred to another device, such as laptop computer 100, through external cable 150. Similarly, data may be transferred from a device such as laptop computer 100, to handheld computing device 125 via external cable 150. External cable 150 represents a detachable general purpose transfer cable, such as a RS-232 based serial cable.

In addition to data transfer, laptop computer 100 and handheld computing device 125 may also be equipped to perform data synchronization using external cable 150.

Data synchronization is a process whereby one or more data files residing on one device are compared to, and if needed, updated to conform with, one or more data files residing on a second device. Sync services 122 represents various synchronization applications that enable such data synchronization between devices.

Figure 2 is a block diagram illustrating a dual form computing device architecture according to one embodiment of the present invention. Referring to Figure 2, dual form computing device 200 represents a single computing device that incorporates the functionality of laptop computer 100 with that of handheld computing device 125, and allows each functionality to be selectively employed. Dual form computing device 200 operates in one of two modes, a low power, "instant on" mode and a high performance, "non-instant on" mode. In the first low power, "instant-on" operational mode, dual form computing device 200 provides instant-on functionality similar to that of handheld computing device 125, whereby a lengthy bootstrap process and operating system load is not required. Unlike handheld computing device 125, however, while dual form computing device 200 is in the instant-on operational mode, a "full size" keyboard device and "full size" display screen, both similar to that of laptop computer 100 may be utilized. In the second high performance, "non-instant on" operational mode, dual form computing device 200 functions substantially similar to laptop computer 100.

Referring again to Figure 2, dual form computing device 200 includes first microprocessor 202, first memory 204, data storage device 206, keyboard 208, display device 210, I/O 212, and first communication device 214. First microprocessor 202 represents one or more a general purpose processors known in the art to process data signals. In one embodiment of the present invention, microprocessor 202 represents a processor belonging to the PENTIUM® family of processors available from Intel Corporation of Santa Clara, California.



First memory 204 represents a volatile data storage device that may store instructions and/or data to be executed by microprocessor 202. Examples of such volatile data storage devices include for example, random access memory (RAM), dynamic random access memory (DRAM), synchronous dynamic random access memory (SDRAM), and the like, available from any one of a number of semiconductor manufacturers, such as Micron Inc., of Boise, Idaho. Additionally, dual form computing device 200 may also contain a read-only memory device (not shown), such as a programmable read-only memory (PROM) or an electronically erasable programmable read-only memory (EEPROM) equipped to store instructions and/or data to be executed by microprocessor 202. Data storage device 206 represents a non-volatile data storage device equipped to store large amounts of data that is accessible by microprocessor 202. Data storage device 206 may include, for example, a hard disk drive, CD-ROM, DVD-ROM, DVD-RAM, DVD-R/W and the like.

Keyboard 208 represents a "full size" alphanumeric input device, while display device 210 represents any "full size" video display device known in the art to display digital data, such as for example, a 15" TFT or active matrix. In one embodiment, display device 210 comprises a touchscreen, whereby a user may input data into dual form computing device 200 by touching or "tapping" the touchscreen with a stylus or similar object. Dual form computing device 200 may also contain other input devices including a mouse, trackball, and touchpad. Additionally, dual form computing device 200 also includes first communication device 214 and second communication device 235. Both first and second communication devices represent input/output communication devices equipped to transfer data within dual form computing device

200. I/O 112, however, represents input and output ports equipped to transmit data to and from devices external to dual form computing device 200.

First operating system 216 may represent any one of the various operating systems known in the art to function with first microprocessor 202. In one embodiment, first operating system 216 is the WINDOWS®-98 operating system available from Microsoft Corporation. In one embodiment, applications 220 represent one or more WINDOWS® 98-based application programs equipped to function under first operating system 216. Sync services 222 represents a set of services equipped to provide interoperability between first operating system 216 and second operating system 241. In one embodiment, Sync services 222 represents the CE services available from Microsoft to sync between a WINDOWS-98 device and a WINDOWS CE device.

Dual form computing device 200 also includes second microprocessor 227, second memory 229, and flash memory 231. Second microprocessor 227 represents a processor known in the art to process data signals. In one embodiment, second microprocessor 227 is a MIPS processor, available from MIPS, Inc., of Milpitas, California.

Second memory 229 and flash memory 231 each represents memory storage devices that store instructions and/or data that may be executed by second microprocessor 227. Second memory 229 may comprise nonvolatile read-only memory (ROM) and/or volatile random access memory (RAM) that may be coupled to a power supply, such as a battery or capacitor (neither shown), to retain stored data after power is removed from dual form computing device 200. Flash memory 231 may comprise a nonvolatile memory device that can be erased and reprogrammed.

Second operating system 241 represents any one of the various operating systems available for use with dual form computing device 200 having second microprocessor 227. In one embodiment, second operating system 241 comprises the WINDOWS CE™ operating system available from Microsoft Corporation. In another  
5   embodiments, second operating system 241 may comprise various other operating systems including PALMOS™ from 3COM Corporation, and EPOC™ from Psion Software Inc.

In one embodiment of the present invention, dual form computing device 200 determines the proper operational mode within which to function, based upon hardware  
10   settings detected at startup. In one embodiment, dual form computing device 200 determines the proper operational mode based upon settings indicated by one or more switching mechanisms (discussed more fully below). In another embodiment, dual form computing device 200 may determine the operational mode within which to function based upon various software settings.

15   The startup process that dual form computing device 200 follows depends upon which of its operational modes is selected. In one embodiment, if an instant-on operational mode is selected, dual form computing device 200 follows a startup process similar to that described above with respect to handheld computing device 125. That is, when dual form computing device 200 is powered-on, second microprocessor 227  
20   immediately processes instructions representing operating system 241 stored within second memory 229 and flash memory 231. A user may immediately input and store data in second memory 229 using WINDOWS CE based applications 243 and full-size keyboard 208. In addition, WINDOWS CE applications 243 and various other data may

be viewed on "full size" video display device 210. Such advantages are not available in the two device approach of the prior art.

If, on the other hand, dual form computing device 200 detects that a non-instant on operational mode is selected, dual form computing device 200 follows a startup process similar to that described above with respect to laptop computer 100. That is, after a successful POST, dual form computing device 200 loads operating system 216 from data storage device 206 into first memory 204 to be executed by first microprocessor 202. As operating system 216 is loaded, device drivers 218 may also be loaded. Once operating system 216 and device drivers 218 (if any) have been loaded and executed, various WINDOWS® based applications 220 may also load and execute. Thus, dual form computing device 200 preserves the advantages of laptop computers while it offers the advantages of handheld computing.

In one embodiment, in addition to first microprocessor 202 functioning while dual form computing device 200 is in the non-instant on operational mode, second microprocessor 227 also functions. With both microprocessors functioning, dual form computing device 200 may automatically synchronize data stored within data storage device 206 and second memory 229. In such a manner, data previously stored while dual form computing device 200 was functioning in instant on operational mode becomes accessible when dual form computing device 200 functions in the non-instant on operational mode. In one embodiment, data synchronization occurs through an internal serial communications link between first communication device 214 and second communication device 235. In another embodiment, data synchronization may occur through a dedicated data transfer bus within dual form computing device 200.

In one embodiment of the present invention, dual form computing device 200 may be implemented through a conventional laptop that has been slightly modified to accept a WINDOWS CE-based add-on card of the present invention. Figure 3 is a block diagram illustrating an add-on card implementation of a dual form computing device according to one embodiment of the present invention. Referring to Figure 3, laptop computer 300 is shown comprising previously described "full size" display device 210, "full size" keyboard 208, and first communications device 214. Likewise, add-on card 305 is shown comprising previously described second communication device 235, second microprocessor 227, second memory 229, and flash memory 231. In one embodiment, first communication device 214 and second communication device 235 are connected by data transfer line 236.

In addition to the previously described components, add-on card 305 also includes display interface 319 and keyboard/mouse interface 317. Similarly, laptop computer 300 additionally includes keyboard/mouse switch 308 and display switch 310. Keyboard/mouse switch 308 comprises a mechanism whereby signal data from keyboard 208 may be selectively routed to components within laptop computer 300 via signal line 312, or to components of add-on card 305 via signal line 313 and keyboard/mouse interface 317. Likewise, data signals may be routed from components of laptop computer 300 and components of add-on card 305 to keyboard 208 by way of signal lines 312 and 313 respectively, and keyboard & mouse switch 308. In one embodiment, keyboard & mouse switch 308 comprises one of a number of mechanical

or electronic switches known in the art such as, for example, a toggle switch, a debounced push-button switch, or a programmable multiplexor.

Display switch 310 also comprises a mechanism whereby video display signal data may be selectively routed from multiple devices. Depending upon the setting of display switch 310, data may be routed to display device 210 from other electronic components of laptop computing device 300 or components of add-on card 305. Data routed from components of laptop computing device 300 passes over signal line 314, through display switch 310 and into display device 210. Data routed from components of add-on card 305 passes through display interface 319, over signal line 315, through display switch 310 and into display device 210. In one embodiment, display interface 319 and keyboard/mouse interface 317 are detachably coupled to laptop computer 300. In a case where display device 210 is equipped with a touchscreen for example, data flow may be reversed, whereby data may be selectively routed from display device 210 to either components of laptop computer 300 or components of add-in card 305 through display switch 310. In one embodiment, display switch 310 comprises one of a number of mechanical or electronic switches known in the art such as, for example, a toggle switch, a debounced push-button switch, or a programmable multiplexor. In a further embodiment, at least one of display switch 310 and keyboard & mouse switch 308 may be implemented via software.

In another embodiment of the present invention, much of the functionality described above with respect to add-on card 305 is incorporated into a single integrated circuit. Figure 4 is a block diagram illustrating a system-on-a-chip implementation of a

dual form computing device according to one embodiment of the present invention.

Referring to Figure 4, add-on card 405 is shown including previously described flash memory 231 and second memory 229, as well as IC 440 of the present invention. IC 440 represents an integrated circuit manufactured according to methods known in the art. Integrated circuit 440 combines the functional aspects of second microprocessor 227, second communication device 235, display interface 319, and keyboard/mouse interface 317 within a single chip having a plurality of functional blocks. Additionally, IC 440 also comprises connector interface 446. Connector interface 446 enables add-on card 405 to communicate with another device, such as laptop computer 400, through a single data connection. Connector 442 represents any connector equipped to transmit data between add-on card 405 and laptop computer 400. In one embodiment, connector 442 is a cable that may be releasably coupled to add-on card 405 through connector interface 446. In another embodiment, connector 442 may represent one or more signal traces between add-on card 405 and laptop computer 400.

In another embodiment, selected ones of functional components from both add-on card 305 and laptop computer 300 are incorporated into one integrated circuit. Figure 5 is a block diagram illustrating an integrated circuit (IC) implementation of a dual form computing device according to one embodiment of the of the present invention. Referring to Figure 5, dual form computing device 500 includes IC 540, first memory 204, second memory 229, display device 210 and keyboard 208, all of which, except for IC 540, were previously described above with respect to Figures 2-4.

IC 540 represents an integrated circuit manufactured according to methods known in the art, and includes first microprocessor block 502, second microprocessor block 527, display switch 510, and keyboard/mouse switch 508. First microprocessor block 502 and second microprocessor block 527 represent processor blocks known in the art to process data. Display switch 510 represents a switch mechanism equipped to selectively route data from either first microprocessor block 502 or second microprocessor block 527 to external display device 210, whereas keyboard/mouse switch 508 represents a switch mechanism equipped to selectively route data from either first microprocessor block 502 or second microprocessor block 527 to external keyboard 208. It should be apparent to one skilled in the art that IC 540 and/or dual form computing device 500 may contain additional components without parting from the spirit and scope of the invention.

In yet another embodiment, rather than the dual form computing device of the present invention utilizing two separate microprocessors, a single, dual mode microprocessor solution may also be implemented. In such an embodiment, a microprocessor having at least two operating modes is utilized in place of two separate microprocessors. Figure 6 is a block diagram illustrating one embodiment of the dual form computing device of the present invention including a single, dual mode microprocessor. Referring to Figure 6, dual form computing device 600 is shown including IC 640, data storage device 606, and first memory device 604, as well as display device 210 and keyboard 208 (both previously described). Data storage device 606 represents a non-volatile data storage device known in the art, whereas first



memory device 604 represents a read only memory and/or random access memory device, however, power is provided to at least a subset of the memory banks of memory 604, sufficient to hold a "reduced function" operating system.

IC 640 is an integrated circuit manufactured according to methods known in the art. IC 640 comprises dual mode microprocessor 602 and O/S switch 610. Dual mode microprocessor 602 represents a general purpose microprocessor having a first high performance mode, whereby the microprocessor operates at high frequency and voltage levels, and having a second low power mode, whereby the microprocessor operates at frequency and voltage levels lower than that of the first high performance mode, thereby consuming less power. O/S switch 610 represents a switching mechanism through which dual mode microprocessor 602 selectively accesses instructions residing within memory device 604 depending upon the operational mode of dual mode microprocessor 602. In one embodiment, O/S switch 610 represents logic equipped to select one or more ranges of memory addresses within first memory device 604. More specifically, a range that is typically powered for instant on mode of operation and a range that is powered on demand for non-instant on mode of operation.

In one embodiment, when dual mode microprocessor 602 operates in a high performance mode, dual mode microprocessor 602 executes instructions representing a first full-featured, large overhead operating system residing in the not always powered portion of first memory device 604. When dual mode microprocessor 602 operates in a low power mode, however, dual mode microprocessor 602 executes instructions representing a second, reduced function or streamlined operating system residing in the typically powered portion of memory device 604. Even though this portion of memory

device 604 is typically powered, power consumption may actually be reduced by the reduced size and complexity of the operating system executed by dual mode microprocessor 602.

In one embodiment, instructions representing the first operating system are loaded from data storage device 606 into first memory device 604 to be executed by dual mode microprocessor 602, whereas in another embodiment, instructions representing the second operating system reside in first memory device 604 and are executed in place by dual mode microprocessor 602. In an alternative embodiment, instructions representing the first or second operating systems may reside in a second memory device powered in a like manner. In one embodiment, the first operating system comprises Microsoft WINDOWS-98, and the second operating system comprises Microsoft WINDOWS CE.

Whereas many alterations and modifications of the present invention will no doubt become apparent to a person of ordinary skill in the art after having read the foregoing description, it is to be understood that any particular embodiment shown and described by way of illustration is no way intended to be considered limiting. Therefore, references to details of various embodiments are not intended to limit the scope of the claims which in themselves recite only those features regarded as essential to the invention.

**CLAIMS**

I claim:

1. An apparatus comprising:

a first plurality of electronic components defining an instant on computing device for use in a first instant on mode of operation;

a second plurality of electronic components, including a plurality of input and output devices defining a non-instant on computing device for use in a second non-instant on mode of operation; and

one or more switching mechanisms coupled to said input and output devices and selectively, to selected ones of said first and second plurality of electronic components to enable said input/output devices to be available for use in said first instant on as well as said second non-instant on mode of operation.

2. The apparatus of claim 1, wherein said first plurality of electronic components includes a first processor to execute instructions representing a first operating system and said second plurality of electronic components includes a second processor to execute instructions representing a second operating system.

3. The apparatus of claim 2, wherein said first plurality of electronic components includes a first memory device and said second plurality of electronic components includes a second memory device, and wherein said first and second processors operate simultaneously to synchronize data between said first and second memory devices.

1 4. The apparatus of claim 1, wherein said first or second plurality of electronic  
2 components includes a processor having at least two operating modes, wherein when in  
3 a first operating mode said processor executes instructions representing a first  
4 operating system, and when in a second operating mode said processor executes  
5 instructions representing a second operating system.

1 5. The apparatus of claim 1, wherein said one or more switching mechanisms  
2 includes a mechanical switch.

1 6. The apparatus of claim 1, wherein said one or more switching mechanisms  
2 includes a digital multiplexor.

1 7. The apparatus of claim 1, wherein said plurality of input and output devices  
2 include a keyboard and a display device.

1 8. An apparatus comprising:  
2 an integrated circuit having a plurality of function blocks for use in a first instant  
3 on mode of operation;  
4 a plurality of electronic components, including a plurality of input and output  
5 devices, said plurality of electronic components for use in a second non-instant on  
6 mode of operation; and  
7 one or more switching mechanisms coupled to said input and output devices and  
8 selectively, to selected ones of said function blocks and said plurality of electronic

9 components to enable said input and output devices to be available for use in said first  
10 instant on as well as said second non-instant on mode of operation.

1 9. The apparatus of claim 8, wherein said plurality of function blocks includes a first  
2 processor to execute instructions representing a first operating system, and wherein  
3 said second plurality of electronic components includes a second processor to execute  
4 instructions representing a second operating system.

1 10. The apparatus of claim 9, further comprising a connector interface to couple said  
2 one or more switching mechanisms to said integrated circuit.

1 11. The apparatus of claim 9, wherein said plurality of function blocks includes a first  
2 memory device, wherein said second plurality of electronic components includes a  
3 second memory device, and wherein said first and second processors operate  
4 simultaneously to synchronize data stored within said first and second memory devices.

1 12. The apparatus of claim 8, wherein said plurality of function blocks or said plurality  
2 of electronic components include a processor having at least two operating modes,  
3 wherein when in a first operating mode, said processor executes instructions  
4 representing a first operating system, and when in a second operating mode, said  
5 processor executes instructions representing a second operating system.

1 13. The apparatus of claim 8, wherein said one or more switching mechanisms  
2 includes a mechanical switch.

1 14. The apparatus of claim 8, wherein said one or more switching mechanisms  
2 includes a digital multiplexor.

1 15. The apparatus of claim 8, wherein said plurality of input and output devices  
2 include a user input device and a display device.

1 16. An integrated circuit comprising:  
2 a first processor block to operate in a first instant on mode of operation;  
3 a second processor block to operate in a second non-instant on mode of  
4 operation; and  
5 one or more switching mechanisms coupled selectively, to selected ones of said  
6 first and second processor blocks to enable data routing between said selected ones of  
7 said first and second processor blocks and a plurality of external devices.

1 17. The integrated circuit of claim 16, wherein said plurality of said external devices  
2 includes a user input device and a display device.

1 18. The integrated circuit of claim 16, wherein said plurality of external devices  
2 includes a first memory device and a second memory device, and wherein said first and

3 second processor blocks operate simultaneously to synchronize data between said first  
4 and second memory devices.

1 19. The integrated circuit of claim 16, wherein said one or more switching  
2 mechanisms includes a mechanical switch.

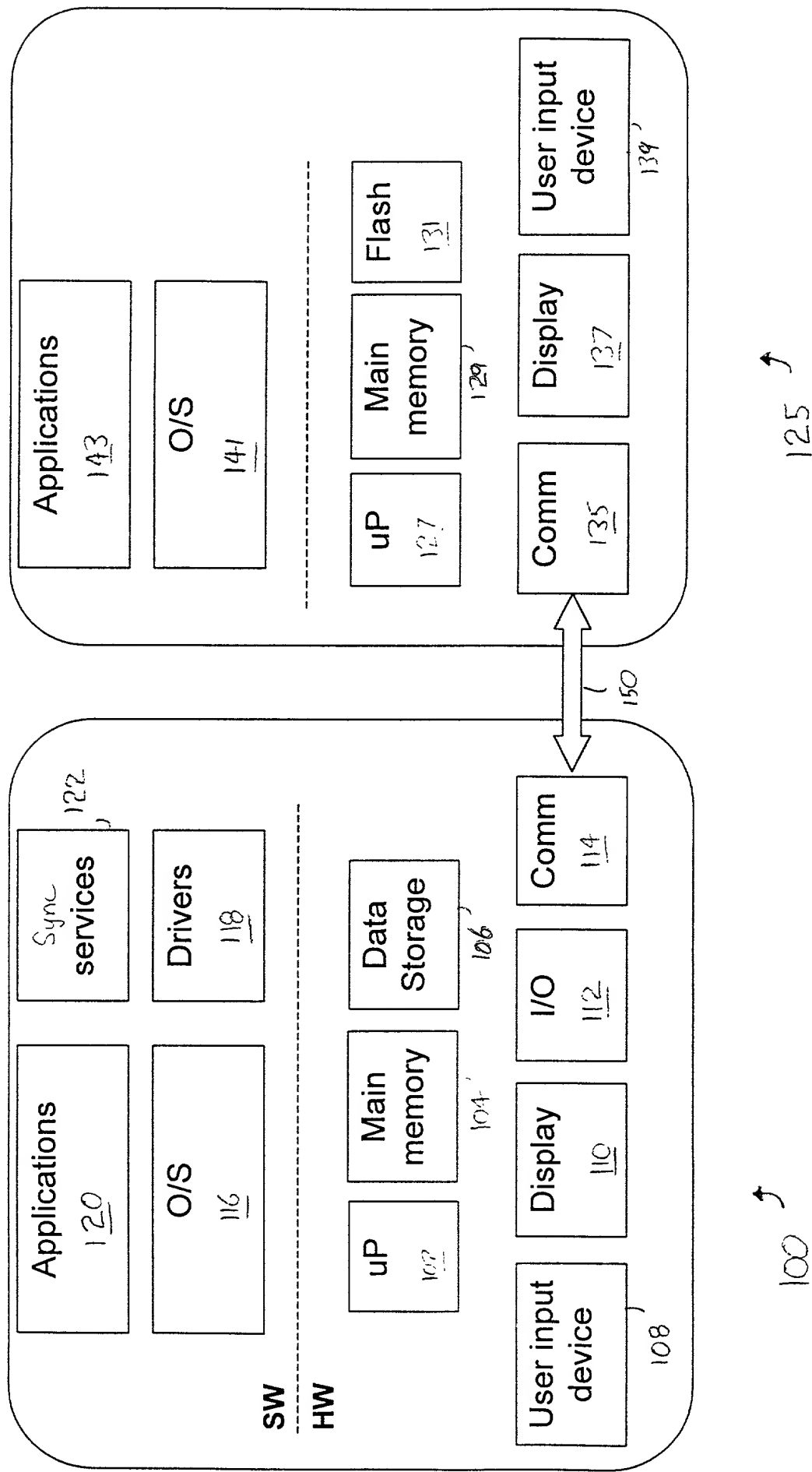
1 20. The integrated circuit of claim 16, wherein said one or more switching  
2 mechanisms includes a digital multiplexor.

1 21. An apparatus comprising:  
2 a memory device having a plurality of memory banks;  
3 a first operating system residing in a first range of said plurality of memory banks;  
4 a second operating system residing in a second range of said plurality of memory  
5 banks;  
6 a dual mode processor having at least two operational modes including:  
7 a first high performance mode, and  
8 a second low power mode; and  
9 logic equipped to selectively provide said dual mode processor with said first or  
10 second operating system in response to said operational mode of said dual mode  
11 processor.

## ABSTRACT OF THE DISCLOSURE

A single, dual form computing device is provided that incorporates the functionality of a laptop computer with that of a handheld or palm-size computing device, and allows each functionality to be selectively employed. The dual form computing device operates in one of two modes. While operating in a first "instant on" mode, the dual form computing device provides functionality similar to that of a handheld device, whereby a lengthy bootstrap process and operating system load is not required. While operating in a second "non-instant on" mode, the dual form computing device operates substantially like a laptop computer. Additionally, the dual form computing device is equipped to share input and output devices independent of the operation mode it functions in.





**FIGURE 1**  
**(PRIOR ART)**

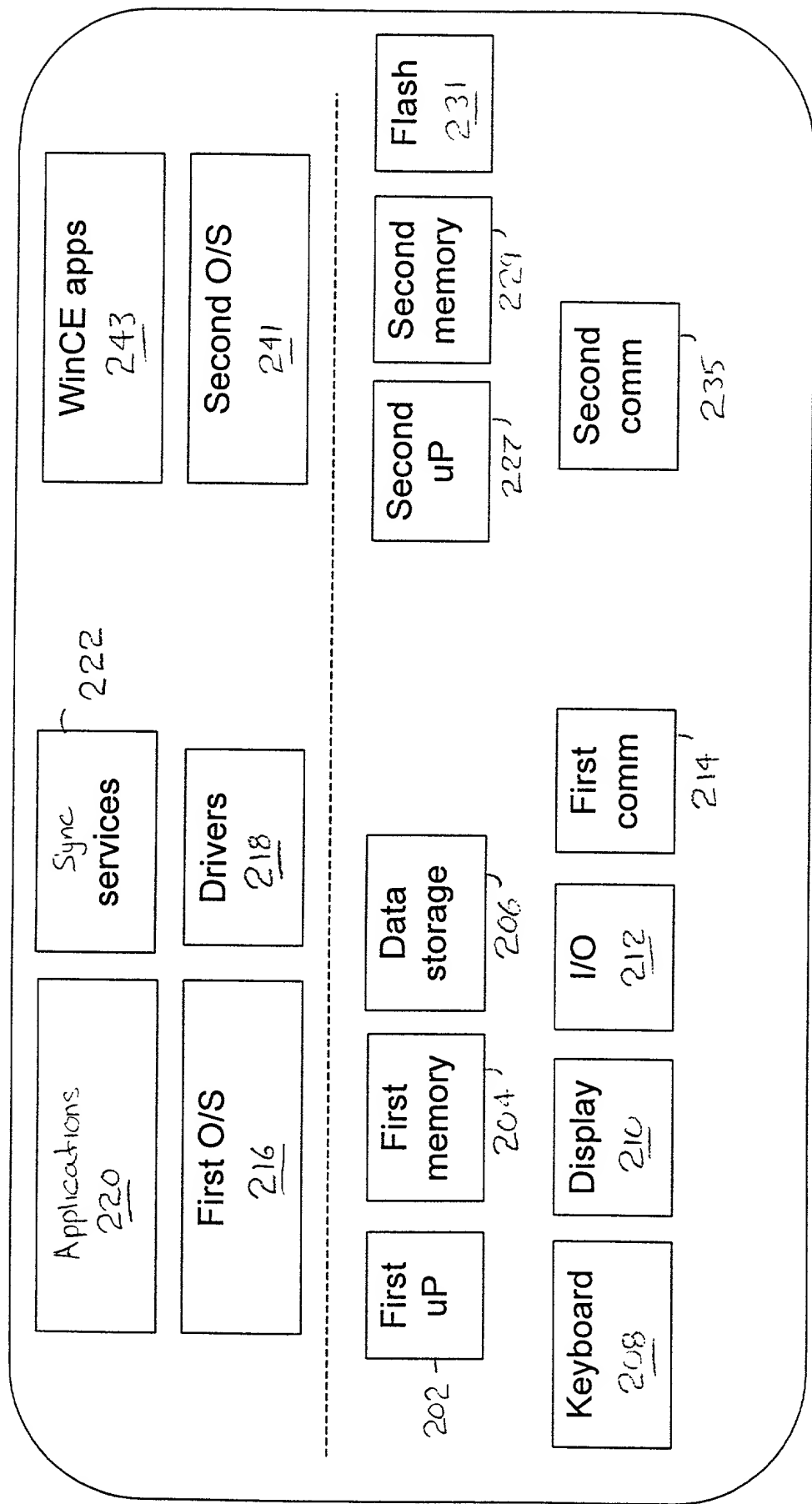


FIGURE 2

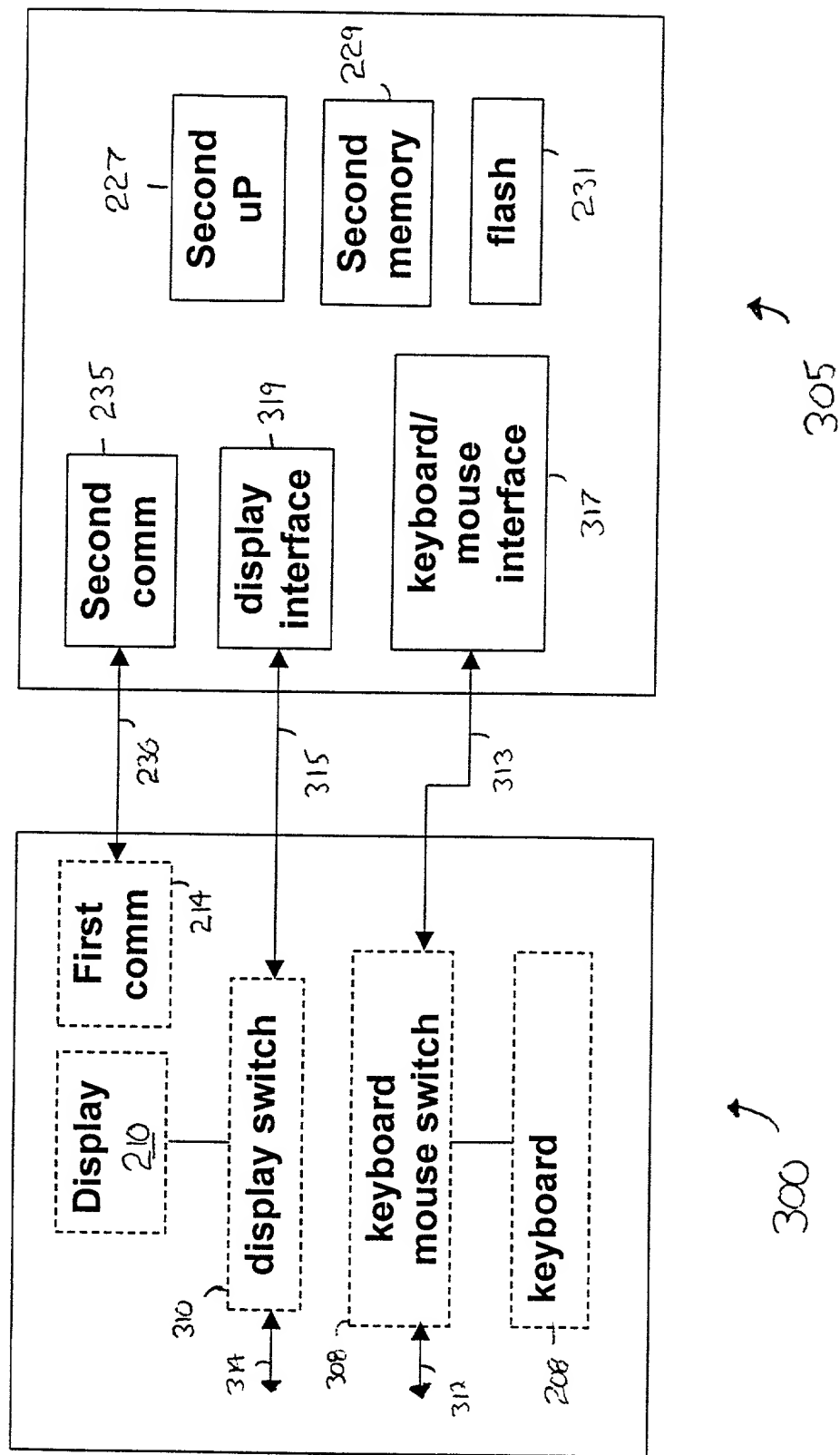


FIGURE 3

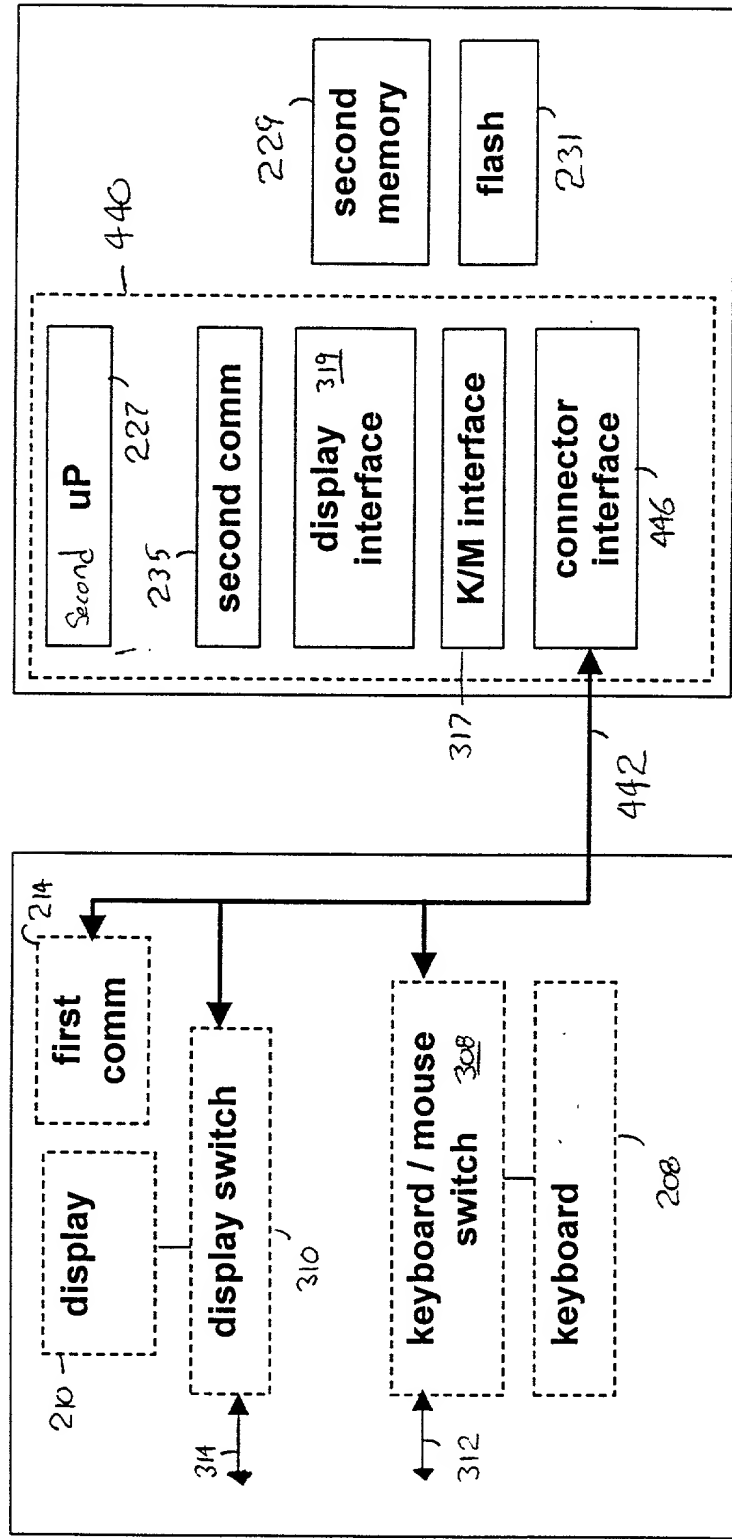
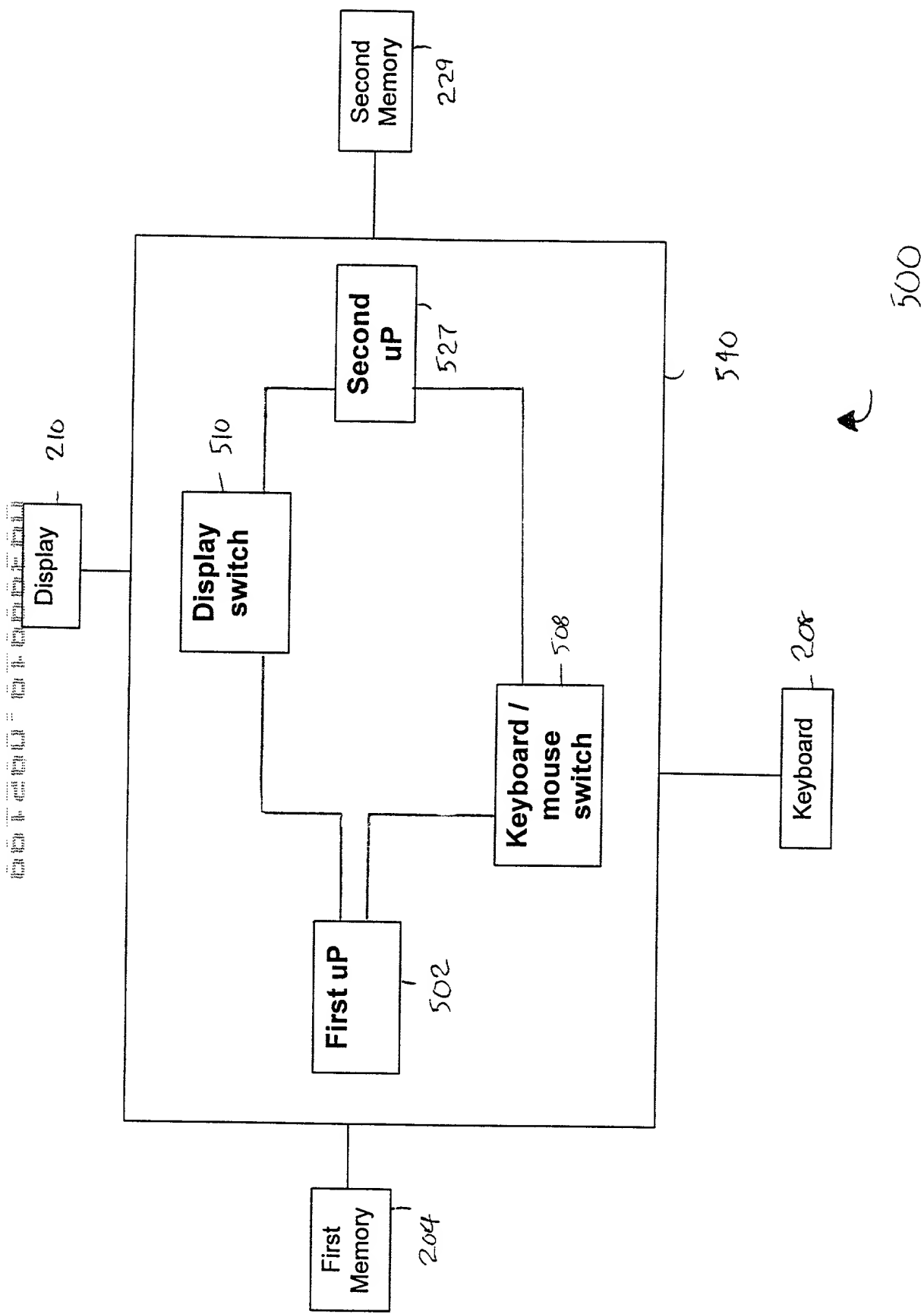


FIGURE 4



**FIGURE 5**

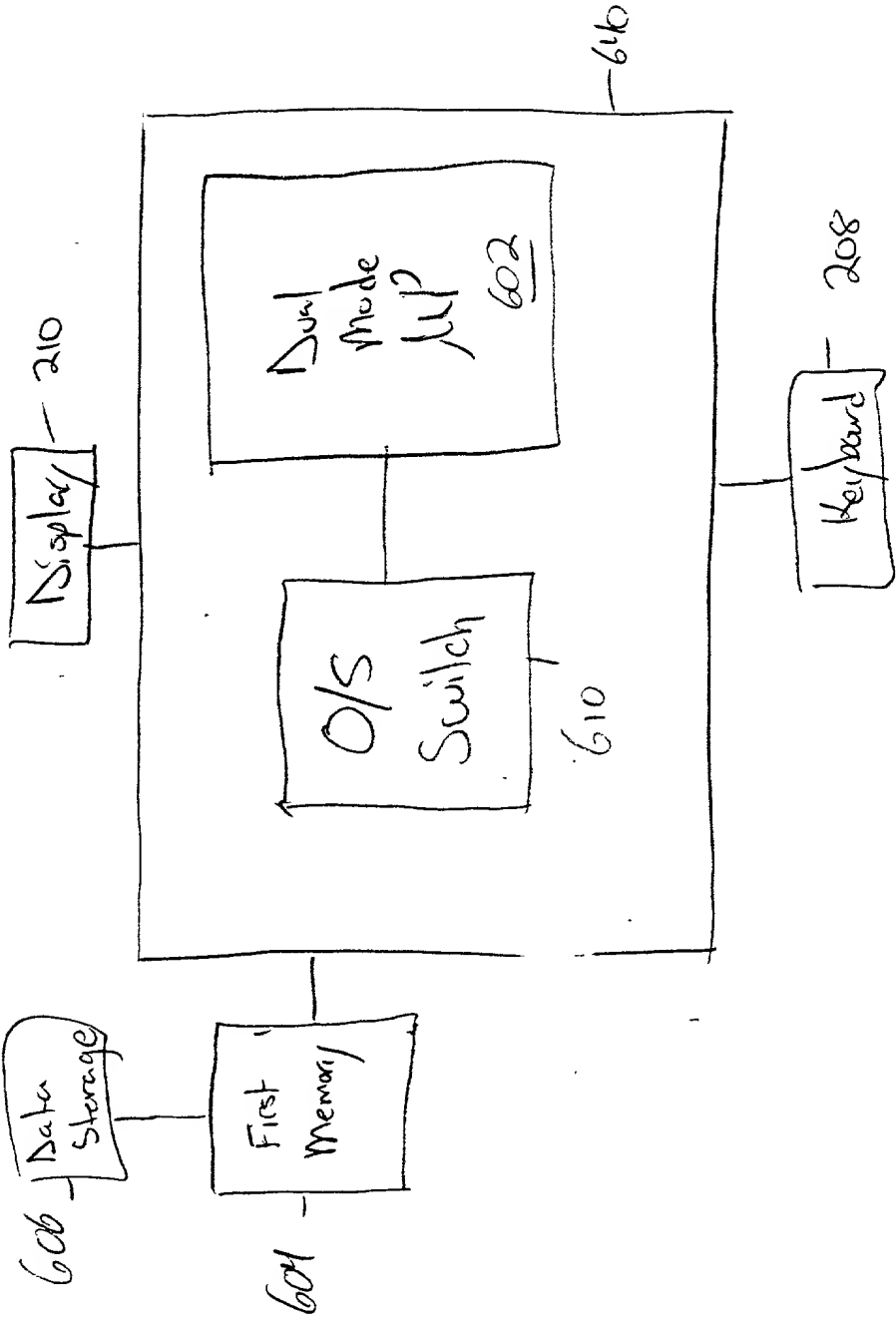


FIGURE 6

**DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION**

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below, next to my name.

I believe I am the original, first, and sole inventor (if only one name is listed below) or an original, first, and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

the specification of which

XX is attached hereto.

\_\_\_\_\_ was filed on \_\_\_\_\_ as

United States Application Number \_\_\_\_\_

or PCT International Application Number \_\_\_\_\_

and was amended on \_\_\_\_\_.

(if applicable)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claim(s), as amended by any amendment referred to above.

I acknowledge the duty to disclose all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119(a)-(d), of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)

Priority  
Claimed

|                   |                    |                                 |            |           |
|-------------------|--------------------|---------------------------------|------------|-----------|
| _____<br>(Number) | _____<br>(Country) | _____<br>(Day/Month/Year Filed) | <u>Yes</u> | <u>No</u> |
| _____<br>(Number) | _____<br>(Country) | _____<br>(Day/Month/Year Filed) | <u>Yes</u> | <u>No</u> |
| _____<br>(Number) | _____<br>(Country) | _____<br>(Day/Month/Year Filed) | <u>Yes</u> | <u>No</u> |

I hereby claim the benefit under title 35, United States Code, Section 119(e) of any United States provisional application(s) listed below:

|                             |                    |
|-----------------------------|--------------------|
| <u>(Application Number)</u> | <u>Filing Date</u> |
| <u>(Application Number)</u> | <u>Filing Date</u> |

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, Section 112, I acknowledge the duty to disclose all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

|                             |                    |   |
|-----------------------------|--------------------|---|
| <u>(Application Number)</u> | <u>Filing Date</u> | <u>(Status -- patented,<br/>pending, abandoned)</u> |
| <u>(Application Number)</u> | <u>Filing Date</u> | <u>(Status -- patented,<br/>pending, abandoned)</u> |

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Attorney's Docket No. 004198.P002

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Send correspondence to Aloysius T.C. AuYeung, BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP, 12400 Wilshire Boulevard 7th Floor, Los Angeles, California 90025 and direct telephone calls to Aloysius T.C. AuYeung, (503) 684-6200.  
(Name of Attorney or Agent)  
(Name of Attorney or Agent)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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